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PTO/SB/21 (6-98) †

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TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

TRANSMITTAL FORM (to be used for all correspondence after initial filing)		Application Number 10/054,124
		Filing Date 01/22/2002
		First Named Inventor Jeffrey Alan Miks
		Group Art Unit 2826
		Examiner Name Williams, Alexander
Total Number of Pages in This Submission 6		Attorney Docket Number AMKOF-003A

ENCLOSURES (check all that apply)

- Fee Transmittal Form
- Fee Attached
- Amendment/Response
- After Final
- Affidavits/declaration(s)
- Extension of Time Request
- Express Abandonment Request
- Information Disclosure Statement
- Certified Copy of Priority Document(s)
- Response to Missing Parts/Incomplete Application
- Response to Missing Parts under 37 CFR 1.52 or 1.53

- Assignment Papers (for an Application)
- Drawing(s)
- Licensing-related Papers
- Petition Routing Slip (PTO/SB/69 and Accompanying Petition)
- Petition to Convert to a Provisional Application
- Power of Attorney, Revocation, Change of Correspondence Address
- Terminal Disclaimer
- Small Entity Statement
- Request for Refund

- After Allowance Communication to Group
- Appeal Communication to Board of Appeals and Interferences
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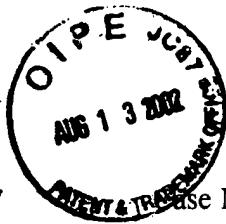
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Case No.: AMKOR-003A
Patent Appln.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jeffrey Alan Miks) Group No.: 2826
Serial No.: 10/054,124) Examiner: Williams, Alexander
Filed: 01/22/2002)
For: SEMICONDUCTOR PACKAGE)
INCLUDING LEADS WITH)
VERTICALLY DOWNSET INNER)
ENDS)

#9/A
Wmst
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J. M. Chinn
9/15/02

AMENDMENT IN RESPONSE TO RESTRICTION REQUIREMENT

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON D C 20231

Dear Sir/Madam:

In response to the Restriction Requirement mailed July 24, 2002, in relation to the above-identified patent application, please amend the application as follows:

In the Claims:

Please cancel Claims 8-16.

Please add the following new claims into prosecution:

18. (New) The leadframe of Claim 17 wherein:

each of the leads has a lead thickness; and

the inner end of each of the leads is vertically downset from the chip mounting pad

at a distance approximately equal to the lead thickness.

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